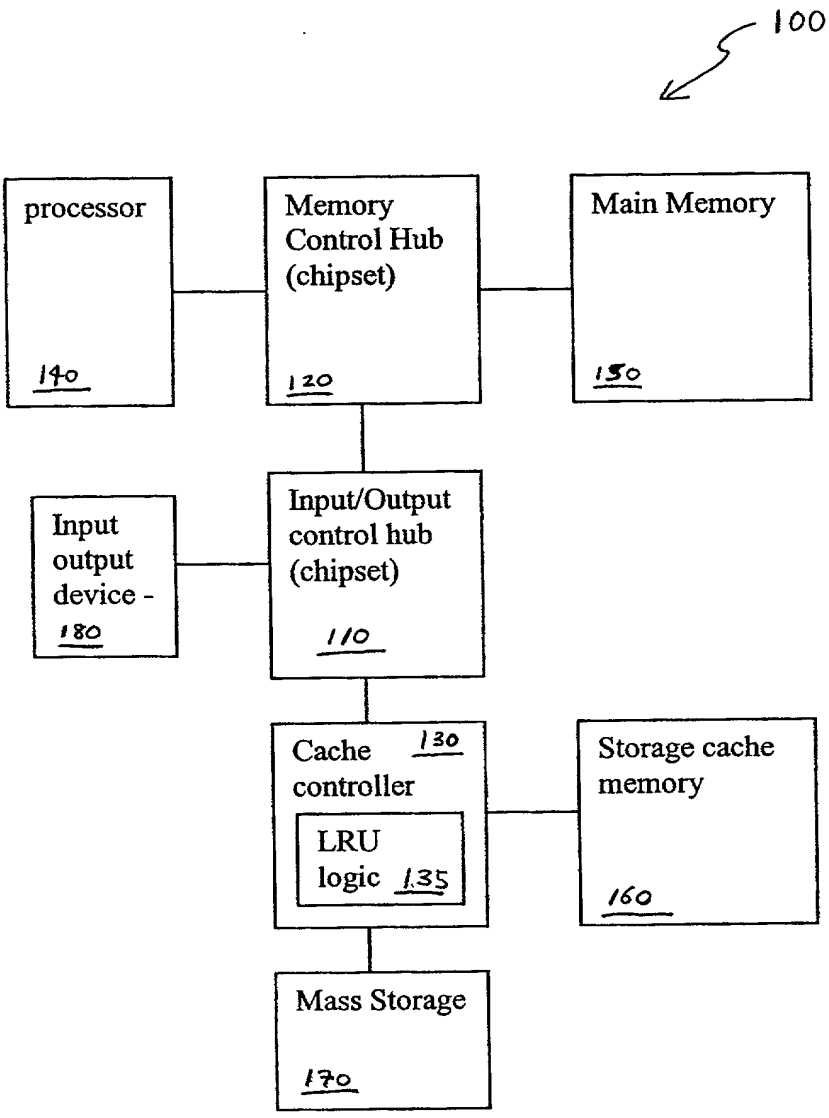
100
processor
140
Memory Control Hub (chipset)
120
Main Memory
150
Input output device - 180
Input/Output control hub (chipset)
110
Cache controller
130
LRU logic 135
Storage cache memory
160
Mass Storage
170

Figure 1

FORE80 9925h660

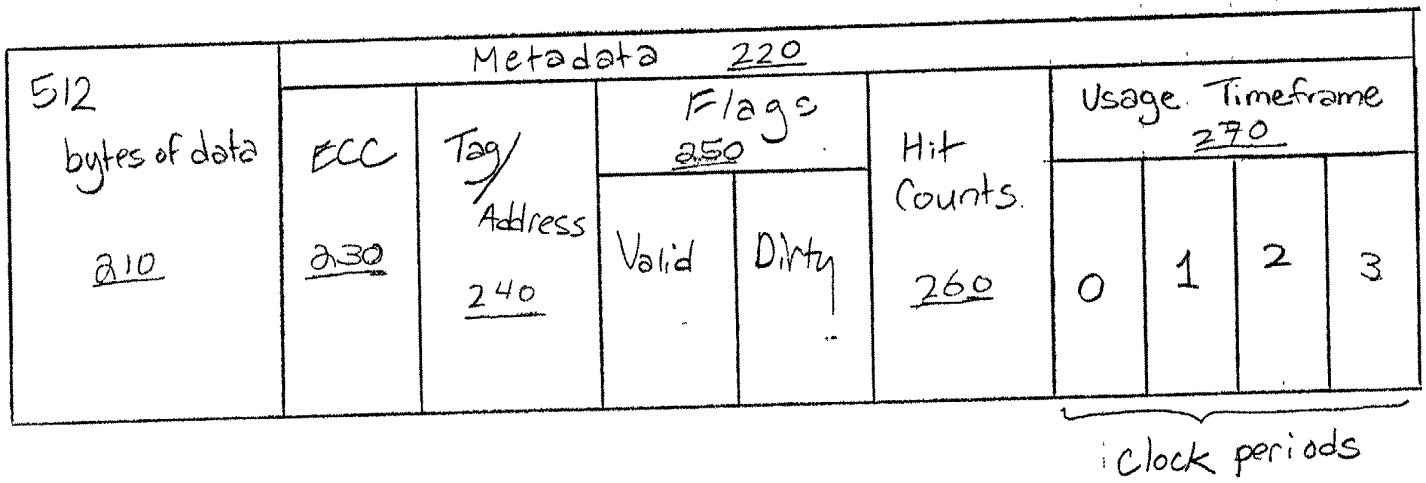

Figure 2

FILE: 0050-0050.TIF

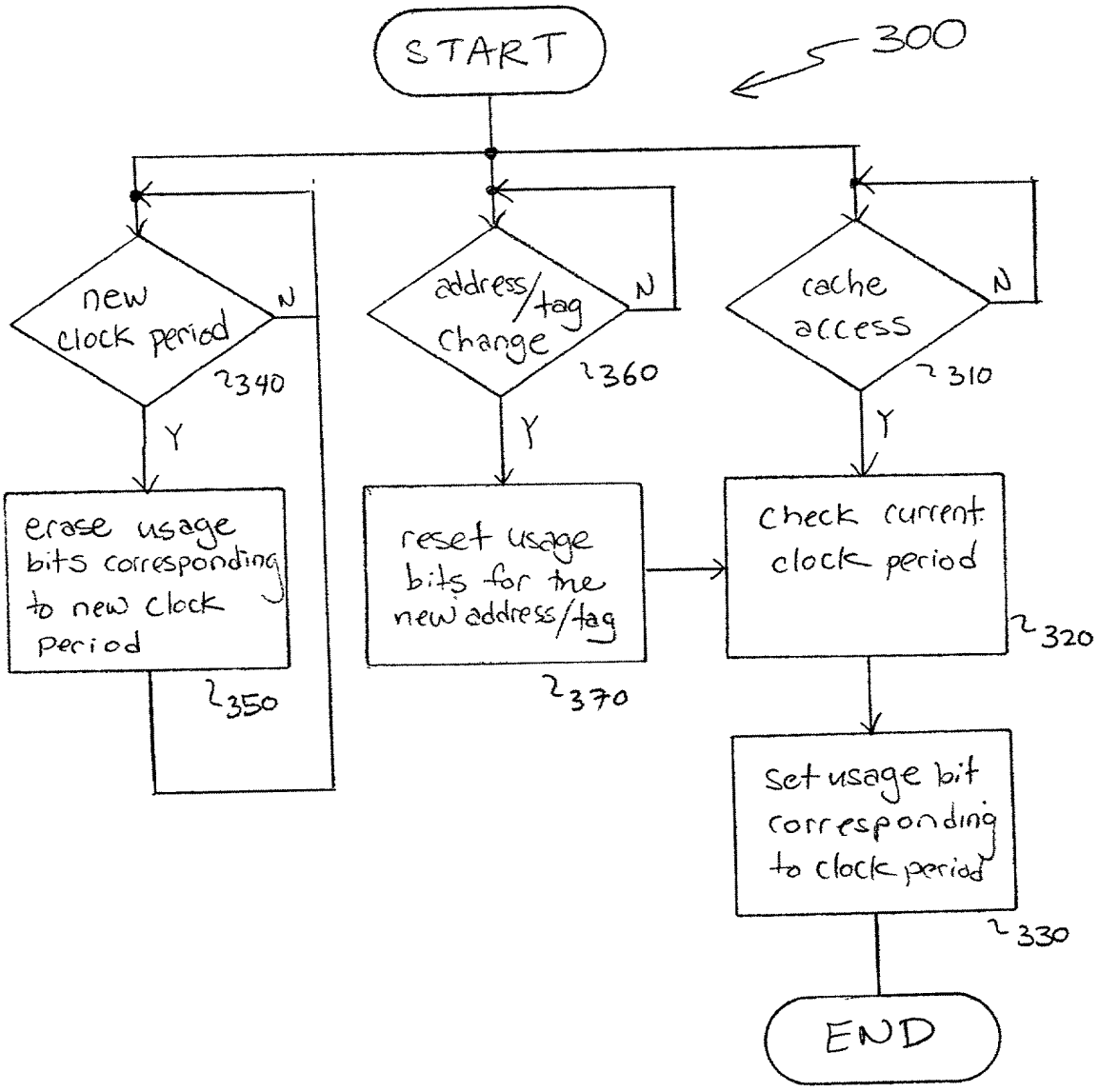

Figure 3